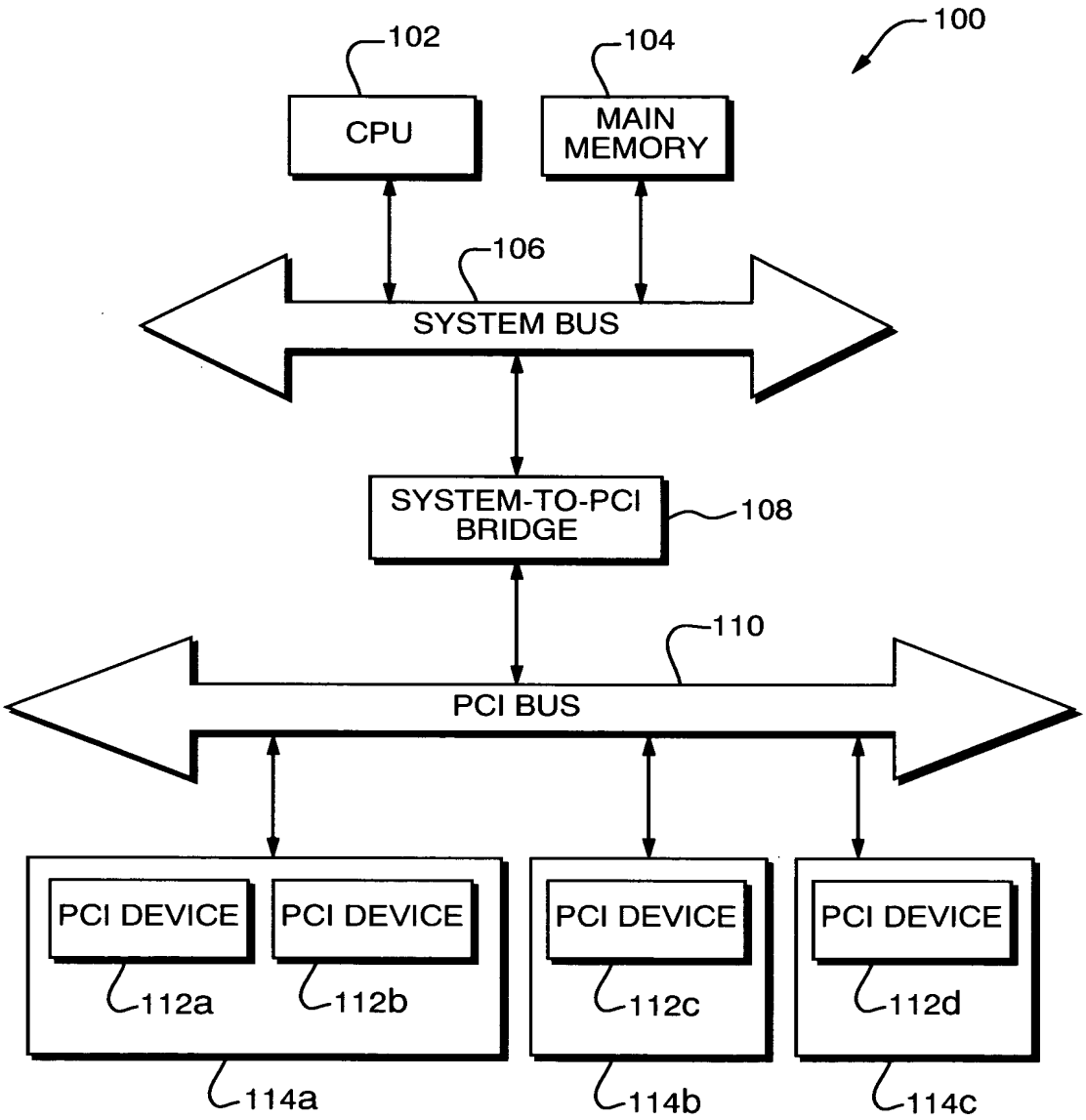

FIG. 1
(PRIOR ART)

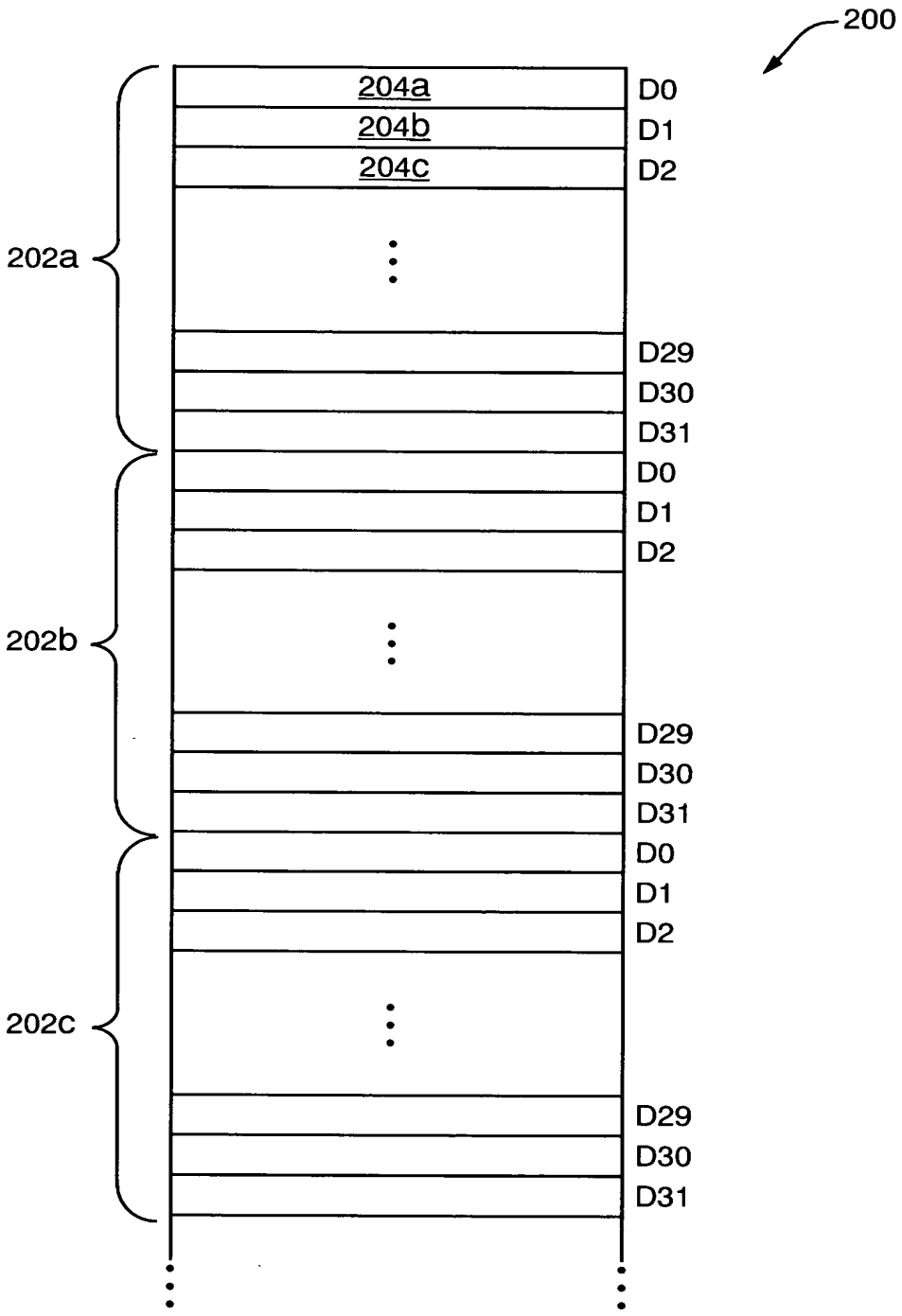

FIG. 2

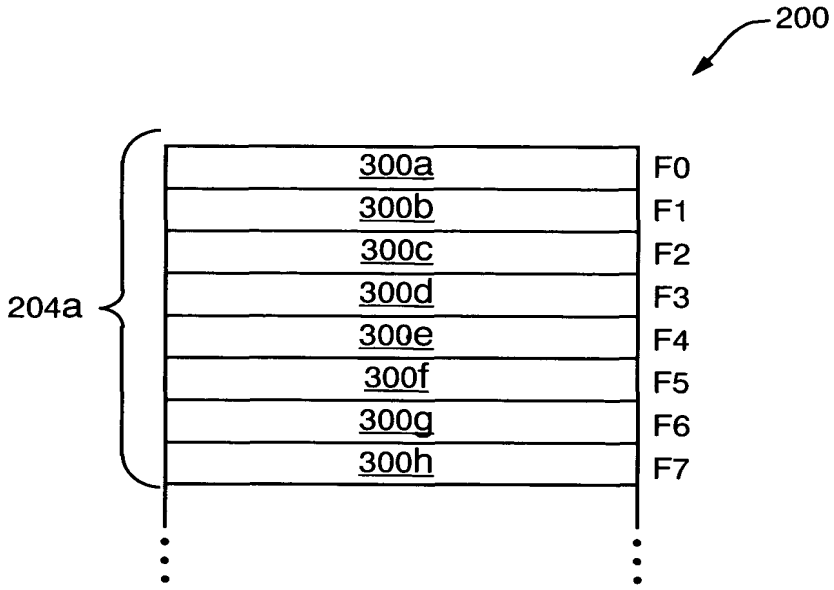
200
300a
300b
300c
300d
300e
300f
300g
300h
F0
F1
F2
F3
F4
F5
F6
F7
204a

FIG. 3

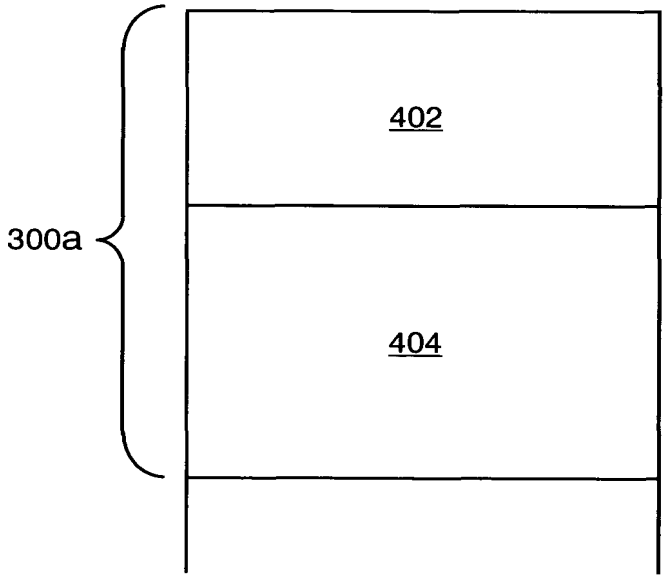
402
404
300a

FIG. 4

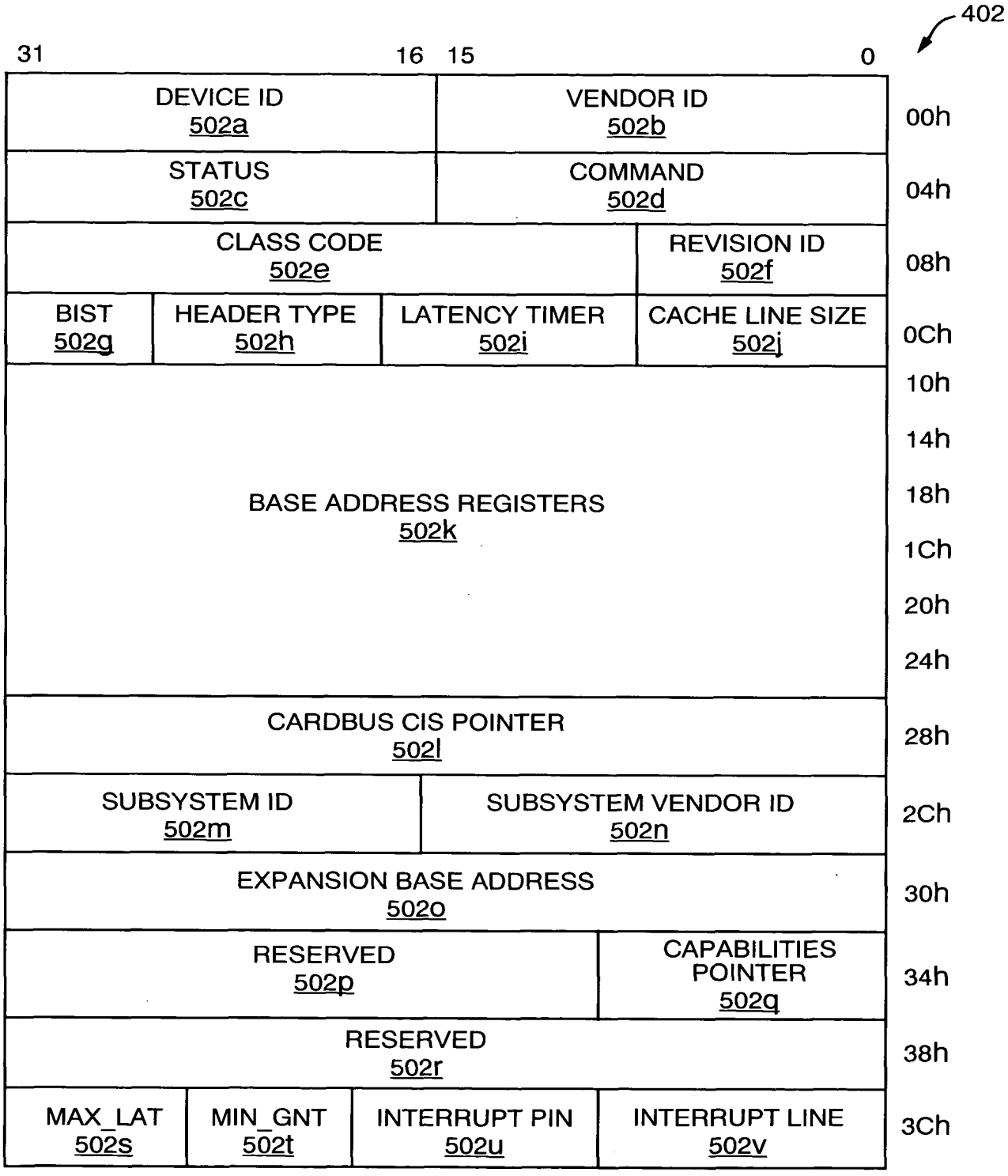
402
31
16
15
0
DEVICE ID 502a
VENDOR ID 502b
00h
STATUS 502c
COMMAND 502d
04h
CLASS CODE 502e
REVISION ID 502f
08h
BIST 502g
HEADER TYPE 502h
LATENCY TIMER 502i
CACHE LINE SIZE 502j
0Ch
BASE ADDRESS REGISTERS 502k
10h
14h
18h
1Ch
20h
24h
CARDBUS CIS POINTER 502l
28h
SUBSYSTEM ID 502m
SUBSYSTEM VENDOR ID 502n
2Ch
EXPANSION BASE ADDRESS 502o
30h
RESERVED 502p
CAPABILITIES POINTER 502q
34h
RESERVED 502r
38h
MAX_LAT 502s
MIN_GNT 502t
INTERRUPT PIN 502u
INTERRUPT LINE 502v
3Ch

FIG. 5

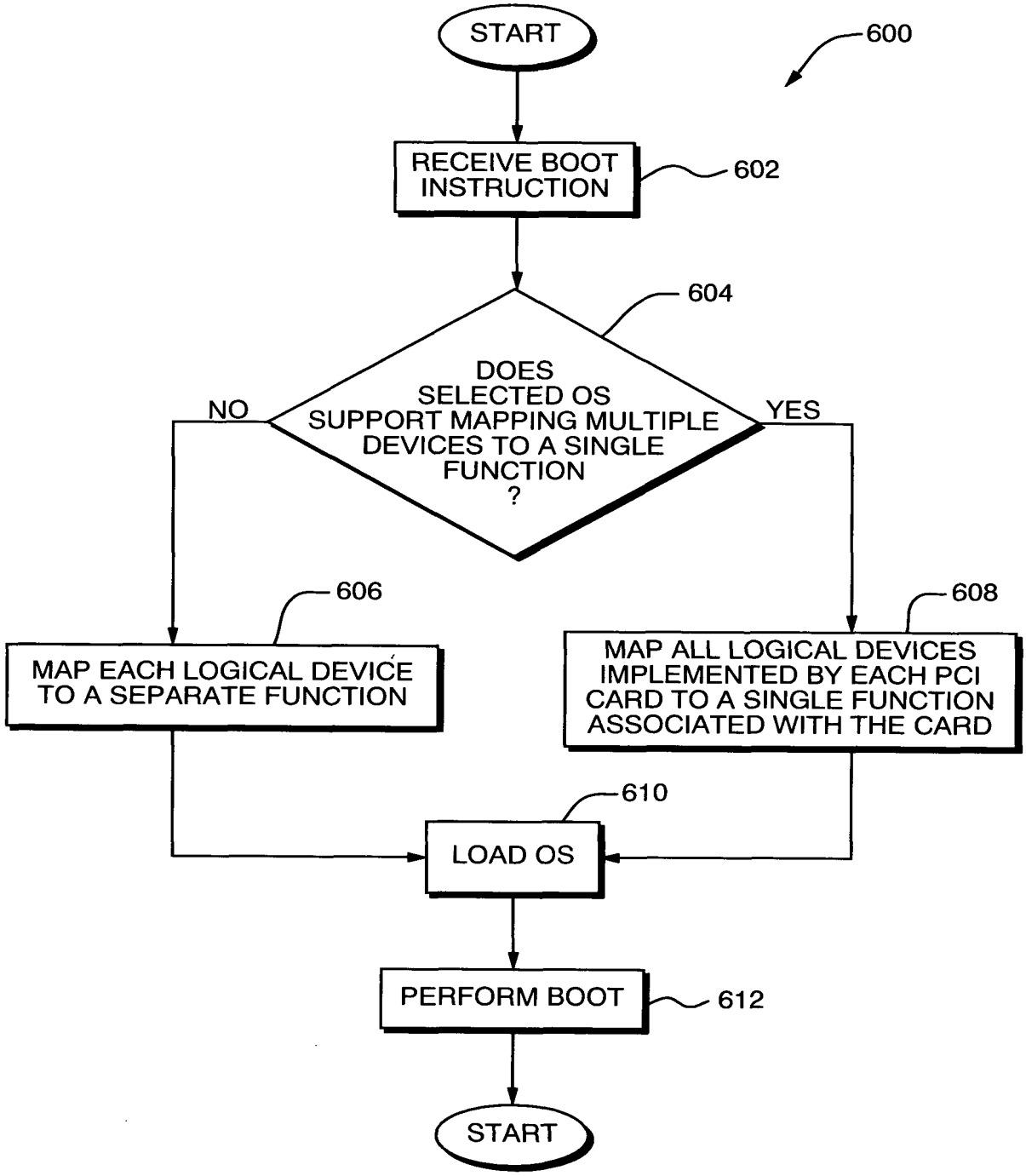
START
600
RECEIVE BOOT INSTRUCTION
602
604
DOES SELECTED OS SUPPORT MAPPING MULTIPLE DEVICES TO A SINGLE FUNCTION ?
NO
YES
606
MAP EACH LOGICAL DEVICE TO A SEPARATE FUNCTION
608
MAP ALL LOGICAL DEVICES IMPLEMENTED BY EACH PCI CARD TO A SINGLE FUNCTION ASSOCIATED WITH THE CARD
610
LOAD OS
PERFORM BOOT
612
START

FIG. 6

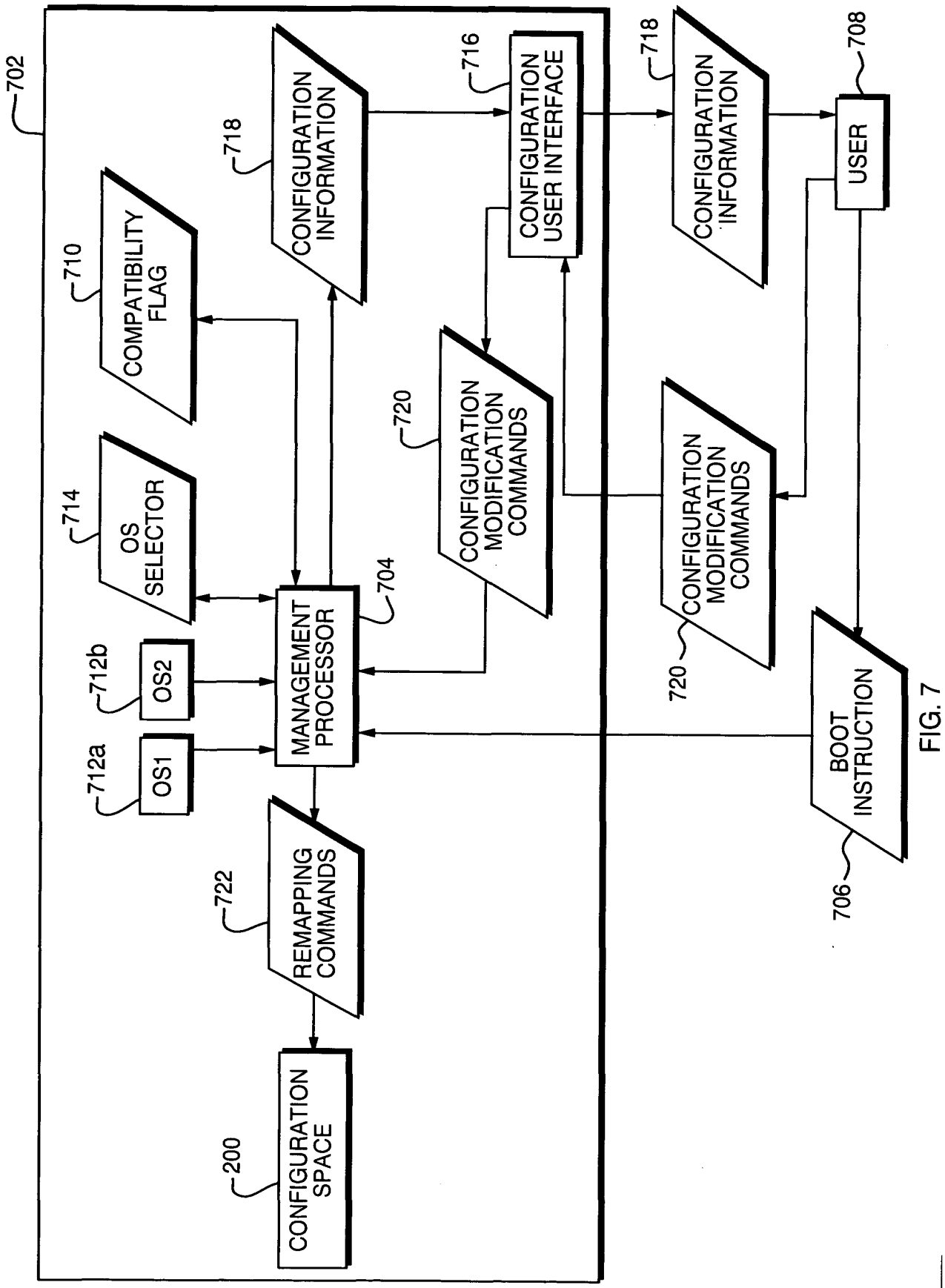
702
718
CONFIGURATION INFORMATION
716
CONFIGURATION USER INTERFACE
718
CONFIGURATION INFORMATION
708
USER
710
COMPATIBILITY FLAG
720
CONFIGURATION MODIFICATION COMMANDS
720
CONFIGURATION MODIFICATION COMMANDS
714
OS SELECTOR
704
MANAGEMENT PROCESSOR
712b
OS2
712a
OS1
706
BOOT INSTRUCTION
722
REMAPPING COMMANDS
200
CONFIGURATION SPACE

FIG. 7

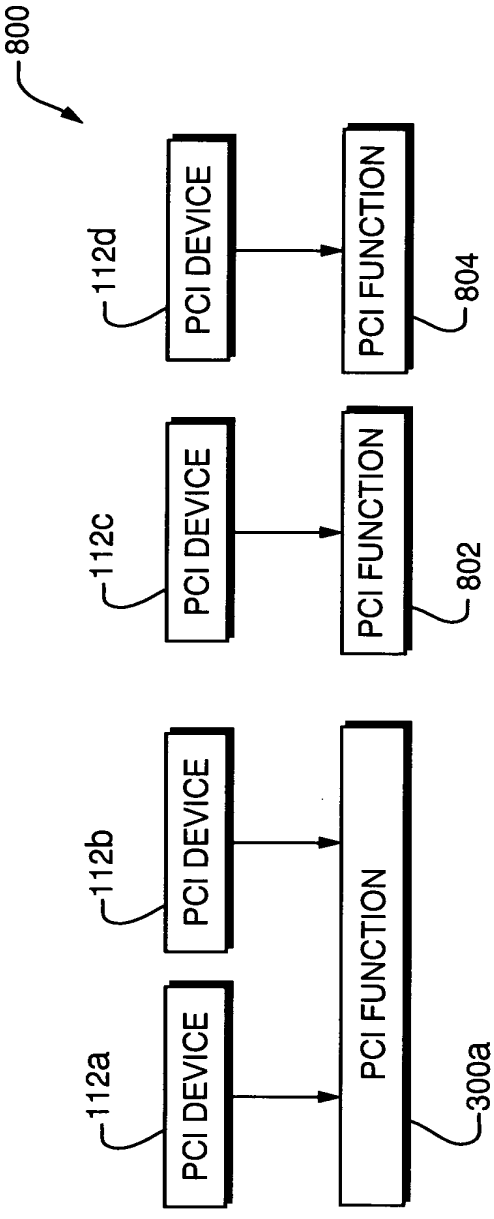
800
112d
PCI DEVICE
PCI FUNCTION
804
112c
PCI DEVICE
PCI FUNCTION
802
112b
PCI DEVICE
112a
PCI DEVICE
PCI FUNCTION
300a

FIG. 8A

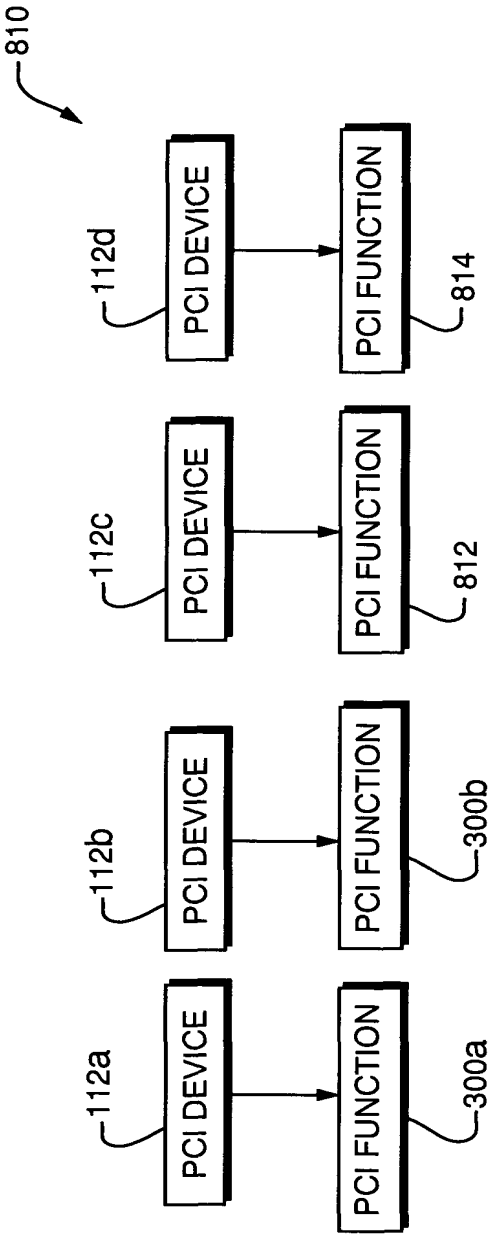
810
112d
PCI DEVICE
PCI FUNCTION
814
112c
PCI DEVICE
PCI FUNCTION
812
112b
PCI DEVICE
PCI FUNCTION
300b
112a
PCI DEVICE
PCI FUNCTION
300a

FIG. 8B